CE 1911

Homework 11

1 – Provide VHDL for a 64KB ROM in a x16 configuration. Assume the memory is initialized by the file "my_rom_file.mif" . Use the inferred memory approach – not a library cell.   60pts

2 – Simulate the ROM from part 1   20pts

3 – Provide a mif file for a 16B ROM in a x16 configuration with address 0 starting at data value 0 and data incremented by 64 each memory location   20pts