

MOS Circuits

Last updated 4/2/22

MOS Circuits

- Simple Inverter - Analysis

Assuming the NMOS device is in saturation

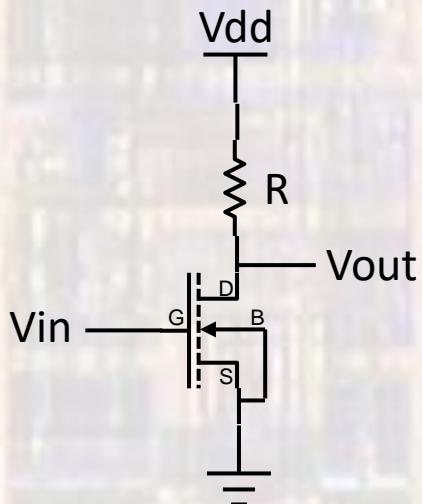
$$I_d = \frac{k'_n}{2} \frac{W}{L} (V_{in} - V_t)^2$$

$$k' = 5\text{mA/V}^2, V_t = 1\text{V}, W/L = 10$$

$$V_{dd} = 3\text{V}, R = 100\Omega$$

$$V_{in} = 1.5\text{v}$$

$$I_d = 6.25\text{mA} \rightarrow V_{out} = 2.375\text{V}$$



Check: $V_{ds} > V_{gs} - V_t$



$$V_{in} = 2.0\text{v}$$

$$I_d = 25\text{mA} \rightarrow V_{out} = 0.5\text{V}$$

Check: $V_{ds} > V_{gs} - V_t$



MOS Circuits

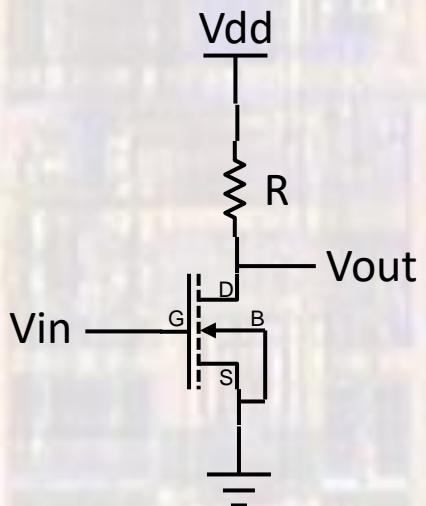
- Simple Inverter - Analysis

Now know NMOS device is not in saturation

$$I_D = \frac{k'_n W}{2 L} [2(V_{GS} - V_{tn})V_{DS} - V_{DS}^2]$$

$$k' = 5\text{mA/V}^2, V_t = 1\text{V}, W/L = 10$$

$$V_{dd} = 3\text{V}, R = 100\Omega$$



$$V_{in} = 2.0\text{v}$$

$$I_D = 22.9\text{mA} \rightarrow V_{out} = 0.71\text{V}$$

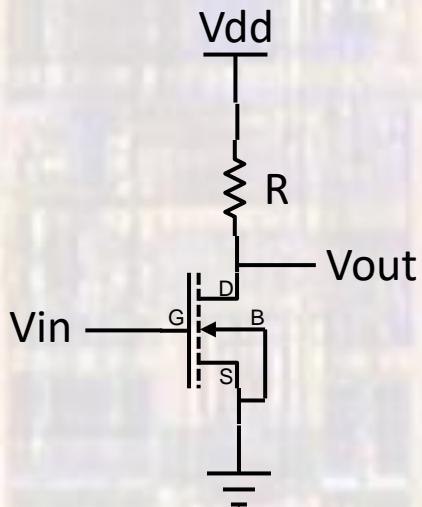
Check: $V_{ds} < V_{gs} - V_t$



MOS Circuits

- Simple Inverter - Design

Assuming Vdd is big enough to keep the NMOS device in saturation at the switching point



$$\frac{V_{dd} - V_{ds}}{R} = \frac{k'n}{2} \frac{W}{L} (V_{ds} - V_t)^2$$

Desire a switching point mid way

$$V_{out} = V_{in} = V_{dd}/2 = V_{ds}/2$$

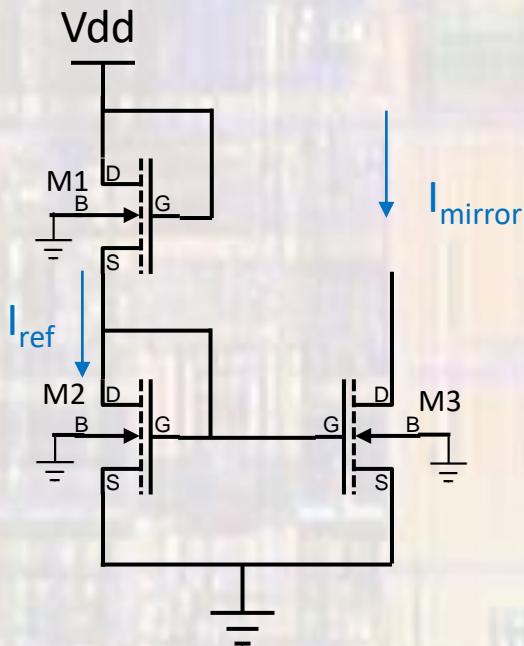
$$\frac{V_{dd}/2}{R} = \frac{k'n}{2} \frac{W}{L} \left(\frac{V_{dd}}{2} - V_t \right)^2$$

$$R = \frac{V_{dd}/2}{\frac{k'n}{2} \frac{W}{L} \left(\frac{V_{dd}}{2} - V_t \right)^2}$$

$$\frac{W}{L} = \frac{V_{dd}/2}{R \frac{k'n}{2} \left(\frac{V_{dd}}{2} - V_t \right)^2}$$

MOS Circuits

- Current Source



Assuming V_{dd} is big enough to keep the NMOS devices in saturation

$$\frac{k'_{n1} W1}{2} (V_{gs1} - V_{t1})^2 = \frac{k'_{n2} W2}{2} (V_{gs2} - V_{t2})^2$$

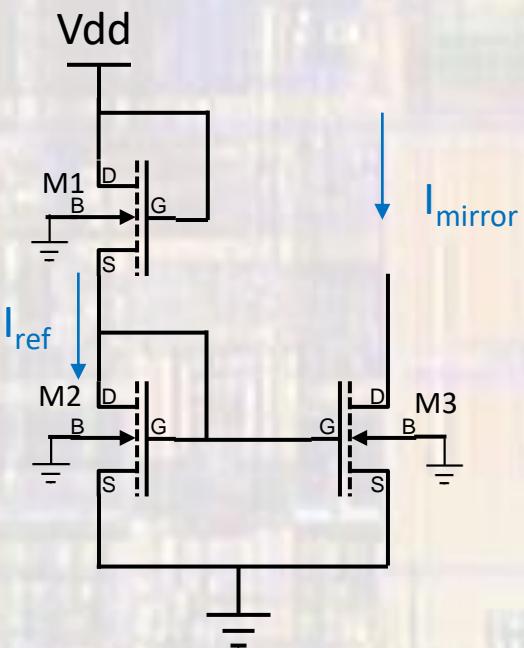
Using similar transistors: $V_{t1} = V_{t2} = V_{t3}$, $k'_{n1} = k'_{n2} = k'_{n3}$
and noting: $V_{gs1} + V_{gs2} = V_{dd}$

$$V_{gs2} = \frac{\sqrt{\frac{W1}{L1} \frac{L2}{W2}} (V_{dd} - V_t) + V_t}{1 + \sqrt{\frac{W1}{L1} \frac{L2}{W2}}}$$

$$I_{ref} = \frac{k'_{n1} W2}{2} \left[\frac{\sqrt{\frac{W1}{L1} \frac{L2}{W2}} (V_{dd} - V_t) + V_t}{1 + \sqrt{\frac{W1}{L1} \frac{L2}{W2}}} - V_t \right]^2$$

MOS Circuits

- Current Source



$$I_{ref} = \frac{k'_n}{2} \frac{W2}{L2} \left[\frac{\sqrt{\frac{W1}{L1}} \frac{L2}{W2} (V_{dd} - V_t) + V_t}{1 + \sqrt{\frac{W1}{L1}} \frac{L2}{W2}} - V_t \right]^2$$

$$I_{mirror} = \frac{k'_n}{2} \frac{W3}{L3} (V_{gs2} - V_t)^2$$

$$I_{mirror} = I_{ref} \frac{\frac{k'_n}{2} \frac{W3}{L3}}{\frac{k'_n}{2} \frac{W2}{L2}} = I_{ref} \frac{W3}{L3} \frac{L2}{W2}$$