

Design Capture

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Design Capture

- Modern electronic circuit designers can use a selection of tools to “capture” their designs
 - Graphical
 - Quartus – Intel FPGAs
 - Multi-sim – free
 - PSpICE – free
 - Cadence Tools – dominate the integrated circuit domain
 - Textual
 - Verilog – common for commercial development
 - VHDL – common for industrial and Gov’t/Military development
 - AHDL/Verilog-AMS – includes analog blocks
 - System-C – C with Hardware

Design Capture

- Graphical design capture (schematic capture) is rare in digital design
 - We will be doing it to:
 - Introduce the process
 - Provide ties between hand designs and simulations
- Graphical design capture (schematic capture) is common for small to medium electronic designs that contain analog, power, or RF components
- Textual design capture dominates the digital design domain
 - We will be using VHDL