

# Comparator Info Sheet

x= 0:1

COMP_Ex	->	CTL0	Control reg 0
		CTL1	Control reg 1
		CTL2	Control reg 2
		CTL3	Control reg 3
		INT	Interrupt Register
		IV	Interrupt Vector

COMP_E0_IRQHandler	INTISR[6]	COMP_E0	CERDYIFG, CEIFG, CEIFG
COMP_E1_IRQHandler	INTISR[7]	COMP_E1	CERDYIFG, CEIFG, CEIFG

## INPUTS

PORT	DIR	PSEL[1:0]	PORT	DIR	PSEL[1:0]
P8.1	C 0 . 0	11	P6.7	C 1 . 0	11
P8.0	C 0 . 1	11	P6.6	C 1 . 1	11
P7.7	C 0 . 2	11	P6.5	C 1 . 2	11
P7.6	C 0 . 3	11	P6.4	C 1 . 3	11
P7.5	C 0 . 4	11	P6.3	C 1 . 4	11
P7.4	C 0 . 5	11	P6.2	C 1 . 5	11
P10.5	C 0 . 6	11	P5.7	C 1 . 6	11
P10.4	C 0 . 7	11	P5.6	C 1 . 7	11

## OUTPUTS

PORT	DIR	PSEL[1:0]	PORT	DIR	PSEL[1:0]
P7.1	COUT	E0	P7.2	COUT	E1
		01			01