

# eUSCI\_TWI Info Sheet

n=3:0

EUSCI_Bn	-> CTLW0	Control Word
	CTLW1	
	BRW	Bit rate divider
	STATW	Status
	TBCNT	Byte Count
	RXBUF	RX Buffer
	TXBUF	TX Buffer
	I2COA0	Own Address Register
	I2COA1	
	I2COA2	
	I2COA3	
	ADDRX	Receive Address Register
	ADDMASK	Address Register Mask
	I2CSA	Slave Address Register
	IE	Interrupt Enable
	IFG	Interrupt Flags
	IV	Interrupt Vector

EUSCIB0_IRQHandler	INTISR[20]	eUSCI_B0	SPI or I2C mode TX, RX, and Status Flags (I2C in multiple-slave mode)
EUSCIB1_IRQHandler	INTISR[21]	eUSCI_B1	SPI or I2C mode TX, RX, and Status Flags (I2C in multiple-slave mode)
EUSCIB2_IRQHandler	INTISR[22]	eUSCI_B2	SPI or I2C mode TX, RX, and Status Flags (I2C in multiple-slave mode)
EUSCIB3_IRQHandler	INTISR[23]	eUSCI_B3	SPI or I2C mode TX, RX, and Status Flags (I2C in multiple-slave mode)

PORT		PSEL[1:0]	PORT	DIR	PSEL[1:0]
P1.6	UC B 0	01	P3.6	UC B 2	01
P1.7	SCL		P3.7	SCL	
P6.4	UC B 1	01	P10.2	UC B 3	01
P6.5	SCL		P10.3	SCL	
			P6.6	UC B 3	10
			P6.7	SCL	10