

eUSCI_UART Info Sheet

n=3:0

EUSCI_An	-> CTLW0	Control Word
	CTLW1	
	BRW	Bit rate divider
	MCTLW	Modulation Control
	STATW	Status
	RXBUF	RX Buffer
	TXBUF	TX Buffer
	ABCTL	Auto Baud Rate Control
	IRCTL	IRDA Control
	IE	Interrupt Enable
	IFG	Interrupt Flags
	IV	Interrupt Vector

EUSCIA0_IRQHandler	INTISR[16]	eUSCI_A0	UART or SPI mode TX, RX, and Status Flags
EUSCIA1_IRQHandler	INTISR[17]	eUSCI_A1	UART or SPI mode TX, RX, and Status Flags
EUSCIA2_IRQHandler	INTISR[18]	eUSCI_A2	UART or SPI mode TX, RX, and Status Flags
EUSCIA3_IRQHandler	INTISR[19]	eUSCI_A3	UART or SPI mode TX, RX, and Status Flags

PORT				PSEL[1:0]	PORT				DIR	PSEL[1:0]		
P1.1	UC	A	0	CLK	01	P3.1	UC	A	2	CLK		01
P1.2				RXD		P3.2				RXD		
P1.3				TXD		P3.3				TXD		
P2.1	UC	A	1	CLK	01	P9.5	UC	A	3	CLK		01
P2.2				RXD		P9.6				RXD		
P2.3				TXD		P9.7				TXD		