

VHDL Type Attributes

- Integer Types (including Enumerated Types)

*type'*left -- left most value

*type'*right -- right most value

*type'*high -- highest value

*type'*low -- lowest value

*type'*pred(value) -- predecessor of value

*type'*succ(value) -- successor of value

*type'*leftof(value) -- left of value

*type'*rightof(value) -- right of value

*type'*pos(value) -- position of value in enumerated type

*type'*val(value) -- value of position in enumerated type

VHDL Type Attributes

- Array Types

*signal'*left -- left most index value
*signal'*right -- right most index value
*signal'*high -- highest index value
*signal'*low -- lowest index value
*signal'*range -- range of array *
*signal'*reverse_range -- range of array *
*signal'*length -- length of array

- With foo defined as unsigned(3 downto 0)
foo'range → 3 downto 0
foo'reverse_range → 0 upto 3