OpAmp Pinout

- ## OpAmp Connections
  - Nucleo-L476RG VINM, VINP, and VOUT connections are fixed
  - Multiple steps are required to access the connections
    - Port(s) must be enabled (clocks enabled) – PortA is enabled by default
    - Pins must be selected as Analog Inputs (using the Pin I/O configuration registers)

Table 150. Operational amplifier possible connections

Signal	Pin	Internal	comment
OPAMP1_VINM	PA1 or dedicated pin[1]	OPAMP1_OUT or PGA	controlled by bits OPAMODE and VM_SEL.
OPAMP1_VINP	PA0	DAC1_OUT1	controlled by bit VP_SEL.
OPAMP1_VOUT	PA3	ADC1_IN8 ADC2_IN8	The pin is connected when the OPAMP is enabled. The ADC input is controlled by ADC.
OPAMP2_VINM	PA7 or dedicated pin[1]	OPAMP2_OUT or PGA	controlled by bits OPAMODE and VM_SEL.
OPAMP2_VINP	PA6	DAC1_OUT2	controlled by bit VP_SEL
OPAMP2_VOUT	PB0	ADC1_IN15 ADC2_IN15	The pin is connected when the OPAMP is enabled. The ADC input is controlled by ADC.

D11
D12

A3

1. The dedicated pin is only available on BGA132 and BGA169 (for STM32L49x/L4Ax devices) package. This configuration provides the lowest input bias current (see datasheet).