## Last updated 6/4/21

- Pulse Width Modulation(PWM)
  - Create a fixed frequency square wave
  - Vary the duty cycle (pulse width) to emulate an analog signal
    mostly on pulses → average = high voltage



mostly off pulses  $\rightarrow$  average = low voltage

- Pulse Width Modulation(PWM)
  - When a PWM signal is fed to a circuit that has a low pass filter characteristic:
    - The high frequency components are removed
    - The low frequency components remain
    - The DC component remains



- Pulse Width Modulation(PWM)
  - DC value is proportional to the duty cycle (pulse width)
    - With a 3.3v signal

