

# SPI Master Class

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Public Member Functions	
	<code>SPI (PinName mosi, PinName miso, PinName sck, PinName ssel=NC)</code> Create a <a href="#">SPI</a> master connected to the specified pins. <a href="#">More...</a>
	<code>SPI (PinName mosi, PinName miso, PinName sck, PinName ssel, use_gpio_ssel_t)</code> Create a <a href="#">SPI</a> master connected to the specified pins. <a href="#">More...</a>
	<code>SPI (const spi_pinmap_t &amp;static_pinmap)</code> Create a <a href="#">SPI</a> master connected to the specified pins. <a href="#">More...</a>
	<code>SPI (const spi_pinmap_t &amp;static_pinmap, PinName ssel)</code> Create a <a href="#">SPI</a> master connected to the specified pins. <a href="#">More...</a>
void	<code>format (int bits, int mode=0)</code> Configure the data transmission format. <a href="#">More...</a>
void	<code>frequency (int hz=1000000)</code> Set the <a href="#">SPI</a> bus clock frequency. <a href="#">More...</a>
virtual int	<code>write (int value)</code> Write to the <a href="#">SPI</a> Slave and return the response. <a href="#">More...</a>
virtual int	<code>write (const char *tx_buffer, int tx_length, char *rx_buffer, int rx_length)</code> Write to the <a href="#">SPI</a> Slave and obtain the response. <a href="#">More...</a>
virtual void	<code>lock (void)</code> Acquire exclusive access to this <a href="#">SPI</a> bus. <a href="#">More...</a>
virtual void	<code>unlock (void)</code> Release exclusive access to this <a href="#">SPI</a> bus. <a href="#">More...</a>

Mode	Polarity	Phase
0	0	0
1	0	1
2	1	0
3	1	1

void	<code>select (void)</code> Assert the Slave Select line, acquiring exclusive access to this <a href="#">SPI</a> bus. <a href="#">More...</a>
void	<code>deselect (void)</code> Deassert the Slave Select line, releasing exclusive access to this <a href="#">SPI</a> bus. <a href="#">More...</a>
void	<code>set_default_write_value (char data)</code> Set default write data. <a href="#">More...</a>
template<typename Type >	
int	<code>transfer (const Type *tx_buffer, int tx_length, Type *rx_buffer, int rx_length, const event_callback_t &amp;callback, int event=SPI_EVENT_COMPLETE)</code> Start non-blocking <a href="#">SPI</a> transfer using 8bit buffers. <a href="#">More...</a>
void	<code>abort_transfer ()</code> Abort the on-going <a href="#">SPI</a> transfer, and continue with transfers in the queue, if any. <a href="#">More...</a>
void	<code>clear_transfer_buffer ()</code> Clear the queue of transfers. <a href="#">More...</a>
void	<code>abort_all_transfers ()</code> Clear the queue of transfers and abort the on-going transfer. <a href="#">More...</a>
int	<code>set_dma_usage (DMAUsage usage)</code> Configure DMA usage suggestion for non-blocking transfers. <a href="#">More...</a>