EE 2920

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Homework 12

1) What value is contained in the master and slave registers after a successful SPI transmission – 2 problems A,B 20pts SCK (CPOL=0) MOSI MISO Master: SEL Ssbar **HEX** CPHA=0 DORD=0 Slave: SCK (CPOL=0) MOSI MISO Master: SEL Ssbar CPHA=0 **HEX** DORD=1 Slave:

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- 2) Write a function to configure the SPI to operate with the following settings assuming the clock is 3 MHz:
 - 10.923KHz baud rate
 - enable SPI to master mode
 - SCK pin idle high with data shifted on the rising edge of SCK
 - transfer data least significant bit first and no interrupts

~ 6 lines of code

3) Use your SPI setup to transfer a "count" value out of the SPI – verify with the analog discovery 60pts

~ 14 lines of additional code

Analog discovery example output

