## EE 2920

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## Homework 7

1) Given a 5 bit D/A with a 3.3 V positive reference and a 0 V negative reference provide the D/A output for the following input codes
$0 \times 02$
$0 \times 0 C$
$0 \times 1 \mathrm{~A}$
2) Given a 7 bit $\mathrm{D} / \mathrm{A}$ with a 2.5 V positive reference and a 0 V negative reference provide the D/A output for the following input codes

0x 24

$0 \times 54$
$0 x 84$
3) Research what INL, DNL and non-Monotonic mean with respect to D/A converters. Draw a simple diagram showing each of these

