EE 3921

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Homework 8

1 – Given the driver code below; P_base is the pio base address, with the pio ports tied to LEDs and configured as outputs only, what is the LED flashing pattern for the code snippet below. Assume usleep is available.

40pts

```
#include "alt_types.h"
#include "io.h"

#define pio_fee(base) IORD(base, 0)
#define pio_fie(base, val) IOWR(base, 0, val)
#define pio_foe(base) IORD(base, 3)
#devine pio_fum(base) IOWR(base, 3, 0x0F)

void pio_foo(alt_u32 base, alt_u8 val);
```

```
#include "foo_drv.h"

void pio_foo(alt_u32 bse, alt_u8 val){
  int i;
  for (i = 0; i < val, i++){
    pio_fie(bse, 3*i);
    usleep(1000000);
    pio_fie(bse, 0);
    usleep(500000);
}
```

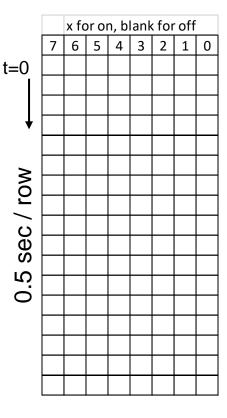
```
...
int ptn = 0;
pio_foo(P_base, 5);
}
...
```



foo_dvr.c

code snippit

LED Pattern



- 2 In EE2920 you needed to use a digital line sensor. The sensor required you to pull a pin high for 100us, then change the same pin to an input and determine when it fell below the input logic threshold to a 0. 60pts
- a) Identify the Platform Designer parameters for an 8 bit PIO to support this operation.
- b) Write a snippit of code to force the pin(bit 4) high for 100us, and then release the pin. Include a test for when the input is recognized as a 0 with approximately 10us resolution. (you can assume usleep() is available)