EE3921 Digital Systems

Lab Practical Directions

Name: _____

♦ **Date: Week 9 Lab Period**
♦ **Duration: 1 hours, 40 minutes + 10min for documentation**

Name: _____

Signature: _____

- This exam is open book and open notes.
- Laptops may be used for code development.
- **Access to the email/messaging/texting of any type is prohibited**
- You may not speak with any other students in the class. You may not receive help from any other person.
- You may use code examples distributed during lecture. You may use code that you wrote as part of previous lab assignments.
- You may use any of the reference materials, user's guides, and datasheets provided on the official course website.
- By signing your name above, you agree to adhere to these policies. Any violations of these policies will result in an F on this exam and referral as a violation of academic integrity.

    Exam Procedure:

- This exam asks you to implement two digital systems. You must properly configure the hardware and then develop the corresponding software as required.
- You must bring a DE10_Lite board and cable.
- You must demo (i.e., checkoff) the working system to your professor.
- The exam will end at the 100-minute time limit. At that time, you must immediately stop working. Any remaining checkoffs will be completed at that time. All students must document their deliverables before leaving the lab. (Print as you go)
- To earn maximum credit, your solutions must:
    o exhibit good design and VHDL coding techniques with comments
    o be fully compliant with the specifications and requirements
- Partial credit may be awarded for systems that exhibit minor bugs but are otherwise fully functional.