

# EE4981 - Lab W8: Amplifier Layout

1 week total

## **Goals:**

1. Understand the intricacies of analog layout

## **Assignment Description:**

### **Overview:**

Create a layout for your Lab6 differential pair design

It is critical that your design be robust to process variations across the die. This means that device parameters can change based on orientation, location, and proximity to other devices.

**Part 1:** Create the best possible design using a 1-1 matching of schematic devices to layout devices

**Part 2:** Create the best possible design – accounting for process variation and device matching

**Part 3:** Do a simple analysis of part 2 – discuss how it reduces the impact of process variations

## **Deliverables:**

Layout plots

Schematic (from Lab 6)

Analysis of process variation impact

**Due: Lab week 9**