EE4981 - Lab W9: DAC design

1 week total

Goals:

1. Create and simulate a DAC

Assignment Description:

Overview:

Create and simulate a design for a R2R DAC
Create and simulate a design for a current DAC

Part 1: Create a 6 bit R2R DAC. Simulate your design and calculate the total power consumed

Part 2: Create a 6 bit current DAC. Simulate your design and determine the linearity of the output

Deliverables:

Schematics
Total power calculation
Linearity plot

Due: Lab week 10