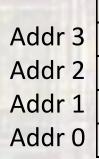
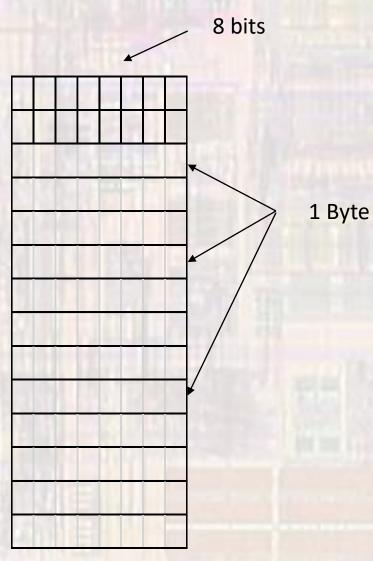
Last updated 6/14/23

These slides describe memory used for data

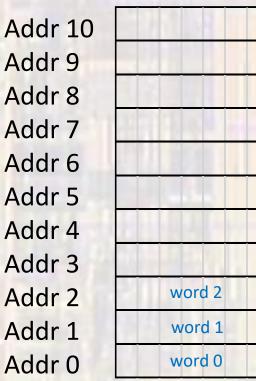
- Integrated Circuit Memory
 - Logical configuration
 - Long column of bytes
 - 1st address is "0"
 - Typically thought of as growing up
 - Sometimes thought of as growing down

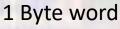


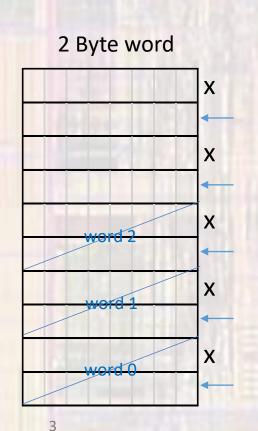


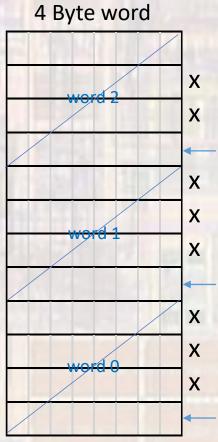
© tj

- Integrated Circuit Memory
 - Word Alignment
 - Allowed addresses indicated by









ELE 1601

- Data Memory
 - Typically, SRAM or DRAM
 - Accessible in Byte, Half-Word, and Word format

