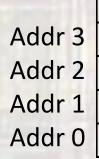
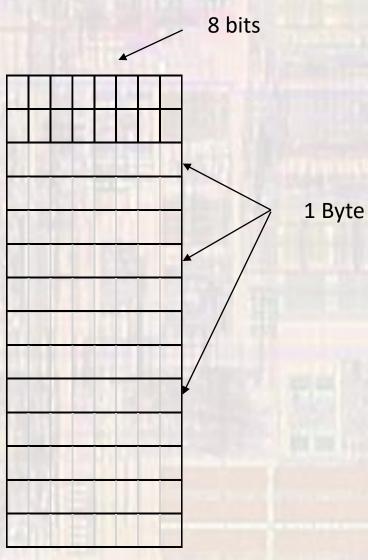
### Last updated 6/14/23

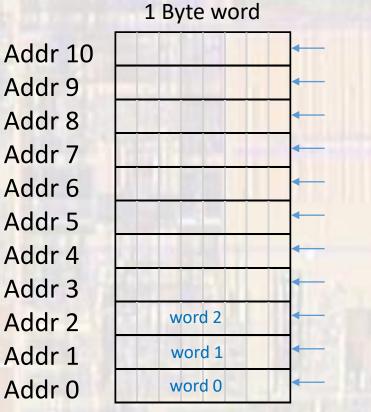
These slides describe memory used for data

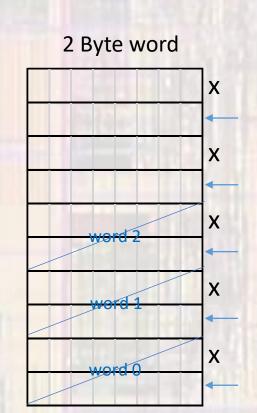
- Integrated Circuit Memory
  - Logical configuration
    - Long column of bytes
    - 1<sup>st</sup> address is "0"
    - Typically thought of as growing up
    - Sometimes thought of as growing down



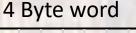


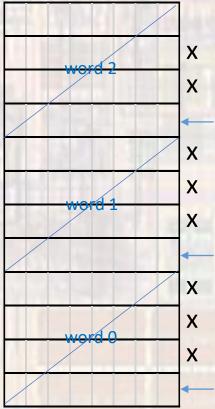
- Integrated Circuit Memory
  - Word Alignment
    - Allowed addresses indicated by





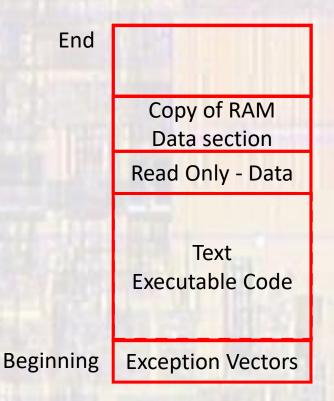
3





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- Program Memory
  - Typically, Flash
  - System dependent word format
    - 16b, 32b, 64b



Initial values for variables

Literals – 3.14159, 1.414, ...

Product code

Reset, exception, and interrupt actions