1A – How many onboard memory blocks would be required to support a 1K word, 16b/word SRAM memory?   10 pts

1B – What is the largest size 32bit word memory that can fit in a single on chip memory block?   10 pts

2 - Implement a dual clock 8KB single port SRAM in by-32 configuration using the Mega Wizard   80 pts

Compile your HDL and provide the code showing the address/data widths and clocks

Provide the compilation summary showing the number of memory bits used