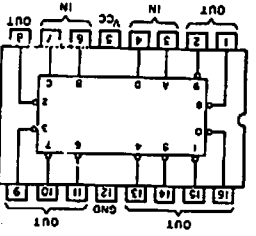
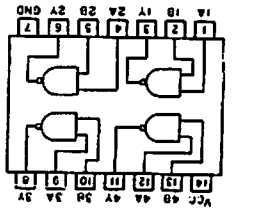


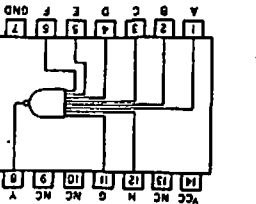
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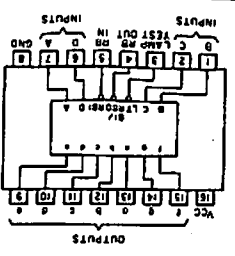
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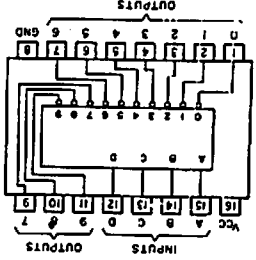
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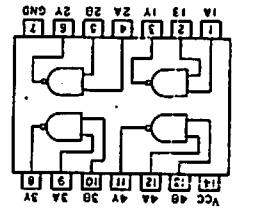
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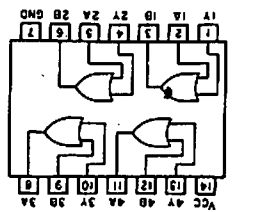
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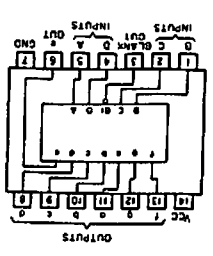
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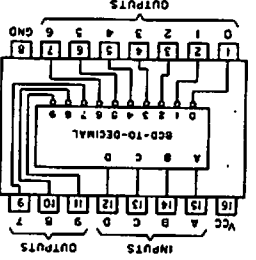
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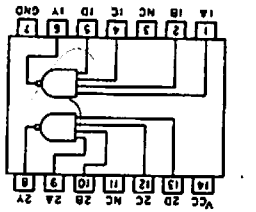
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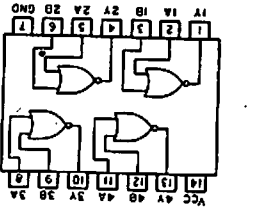
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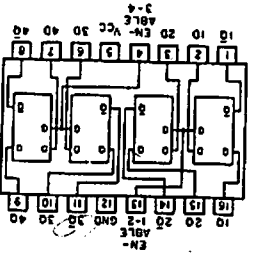
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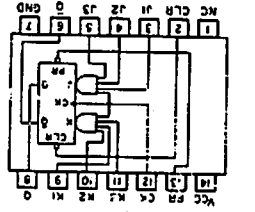
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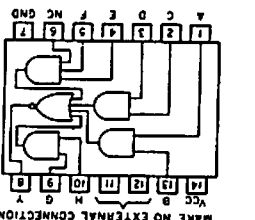
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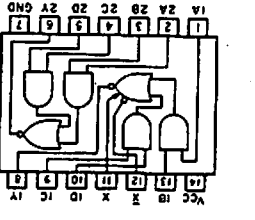
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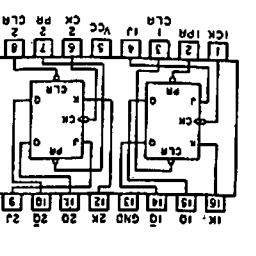
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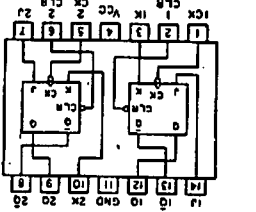
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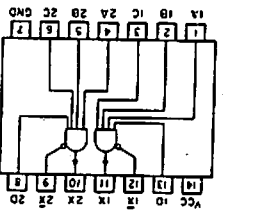
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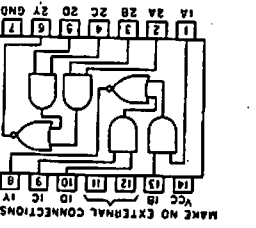
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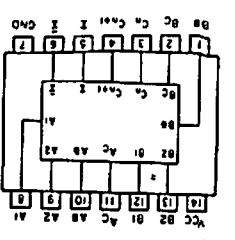
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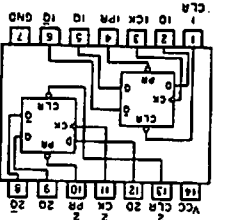
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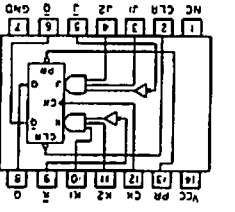
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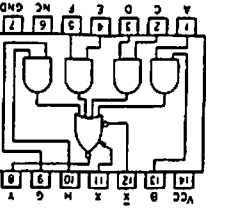
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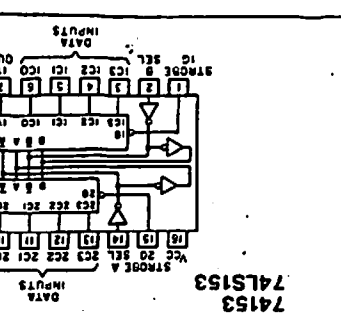
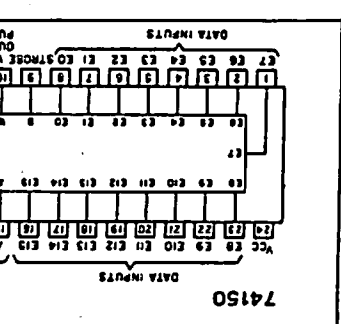
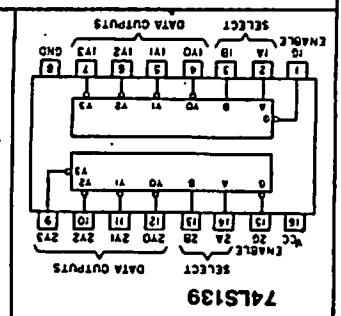
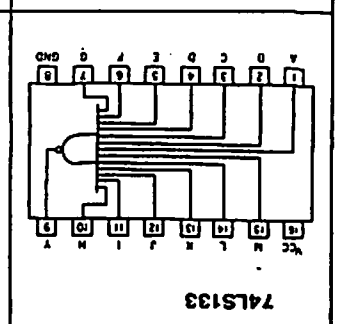
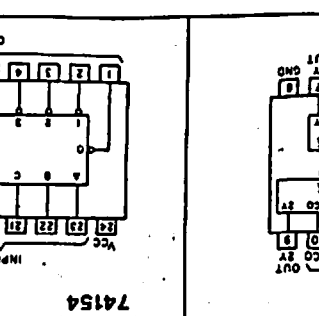
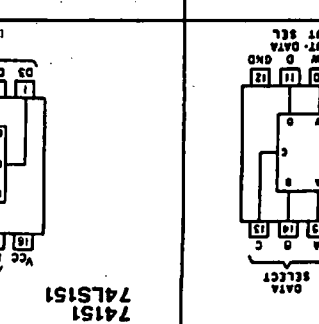
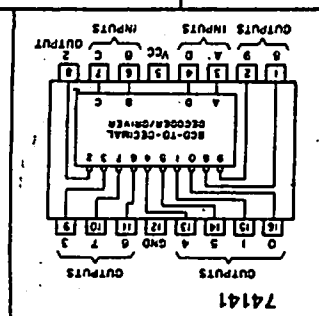
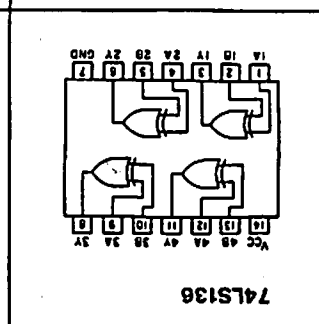
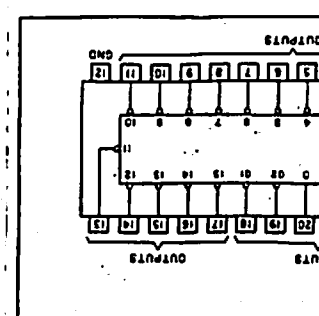
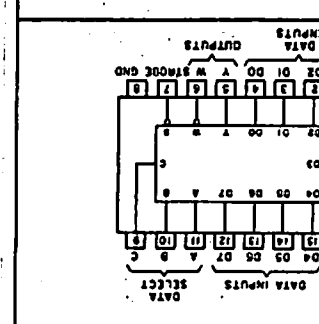
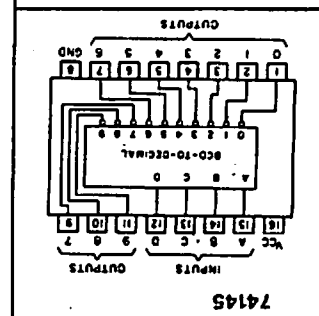
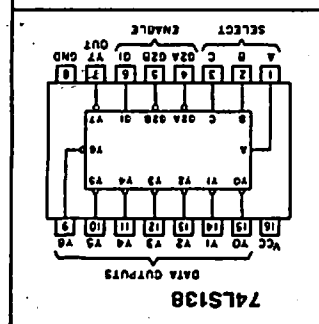
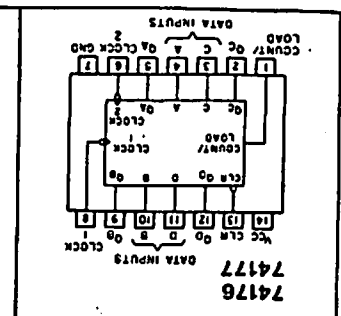
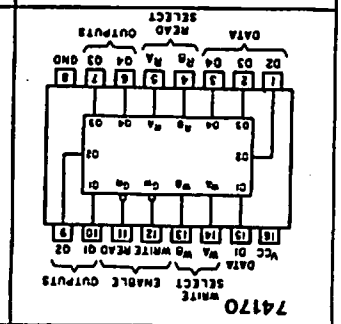
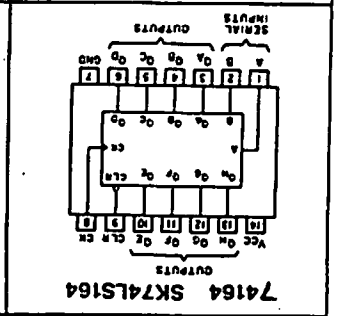
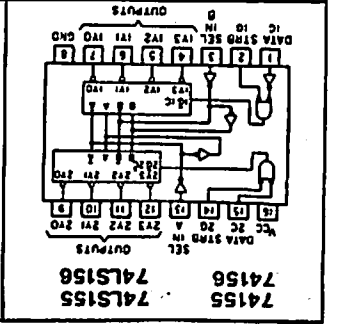
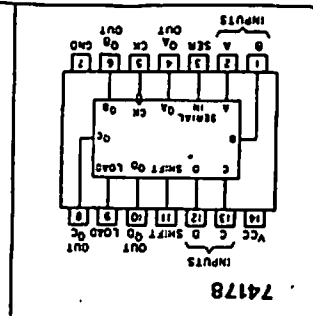
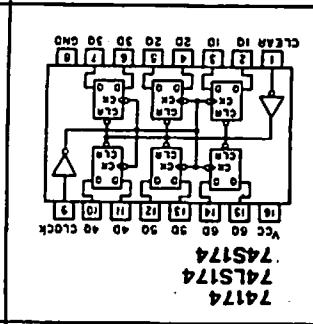
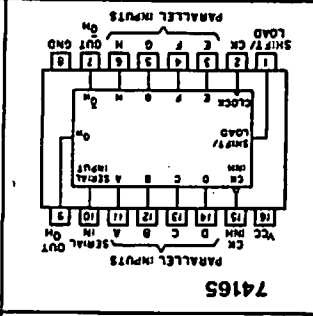
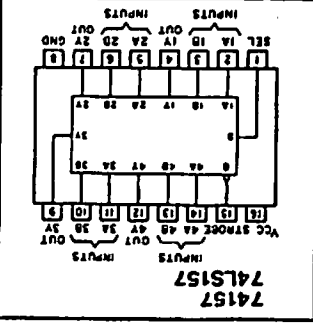
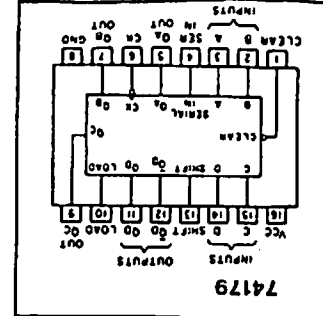
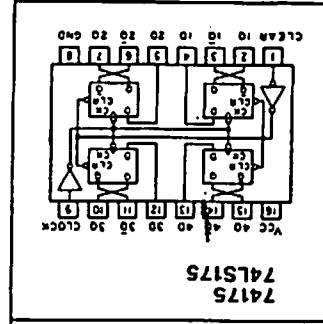
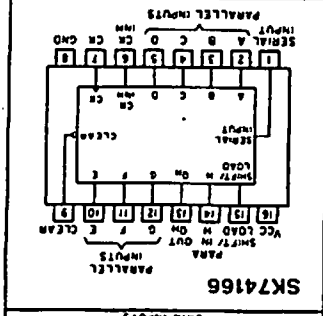
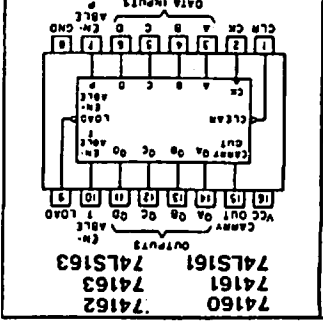
7470



7453



All Diagrams are Top View



TTL Integrated Circuit Terminal Diagrams

All Diagrams are Top View

TTL Integrated Circuit Terminal Diagrams