MSOE EECS Department
CE-1901: Week 3 Lab Grading Checklist
Dr. Yoder   Name:

Item	Points
Prelab 4 Schematic (remember to follow instructions from last week)	/ 2
Prelab 4 Simulation and explanation (remember to follow instructions from last week)	/ 2
Preliminary Reading 11: 7408 AND gate test screenshot (**All** logical analyzer outputs in screenshots should be explained similarly to simulations)	/ 2
Figure 11 Mystery circuit screenshot	/ 3
Figure 12; >4 circuit screenshot	/ 3
Demo 1: Individual: Prelab schematic on DE0	/ 2
Demo 2: Partner: 7408 AND test	**/ 2**
Demo 3: Partner: Figure 11 Mystery circuit	**/ 2**
Demo 4: Partner: Figure 12; >4 circuit	**/ 2**
Total	/ 20

- *Staple* this lab cover sheet on top of all the materials you are submitting, or hand this single page, and email a single pdf with the pages in the correct order.
- Submit everything in the *order* listed above.
- You may omit pages from the lab packet that do not contain work you are submitting.
- Your lab packet is due by 9 AM on the day after the lab is performed. You may do your (late) demonstration after submitting your lab packet if necessary.
- Extra copies of lab checkout sheets are available on my course website.

Acknowledgement: The original version of this checklist was created by Dr. Eric Durant.