MSOE EECS Department
CE-1911: Week 3 Lab Grading Checklist
Dr. Yoder   Name:

Item	Points
3-bit up/down locking counter state diagram	/ 2
3-bit up/down locking counter truth table	/ 2
3-bit up/down locking counter Quartus schematic	/ 2
3-bit up/down locking counter simulation. Whenever you print out a simulation result, be sure to mark it up to demonstrate that it meets the requirements. Draw arrows to where you would point while describing it to me, and hand-write text explaining how that part of the simulation result demonstrates that the circuit meets the requirements.	/ 2
3-bit up-even/down-odd counter state diagram	/ 2
3-bit up-even/down-odd counter truth table	/ 2
3-bit up-even/down-odd counter Quartus schematic	/ 2
3-bit up-even/down-odd counter simulation. Be sure to annotate this simulation using the instructions above.	/ 2
Demonstrate the simulations on your screen during the lab period.	/ 2
Following Submission Instructions	/ 2
Total	/ 20

- **Staple** this lab cover sheet on top of all the materials you are submitting.
- In addition to the materials above, submit any other supporting materials you create while working the lab (sketches, truth-tables, etc.) where they fit best in your report.
- Submit your work in the **order** listed above.
- Your demo is due during the lab period. Your lab packet is due by 9 AM on the day after the lab is performed. You may do your (late) demonstration after submitting your lab packet if necessary. There is a 1 point per day late penalty on the demo. The maximum late penalty for the report+demo per day is 2 points. Slip your submitted lab packet under my office door or submit your packet to me during the laboratory.